

Fig. 1

FIG. 2

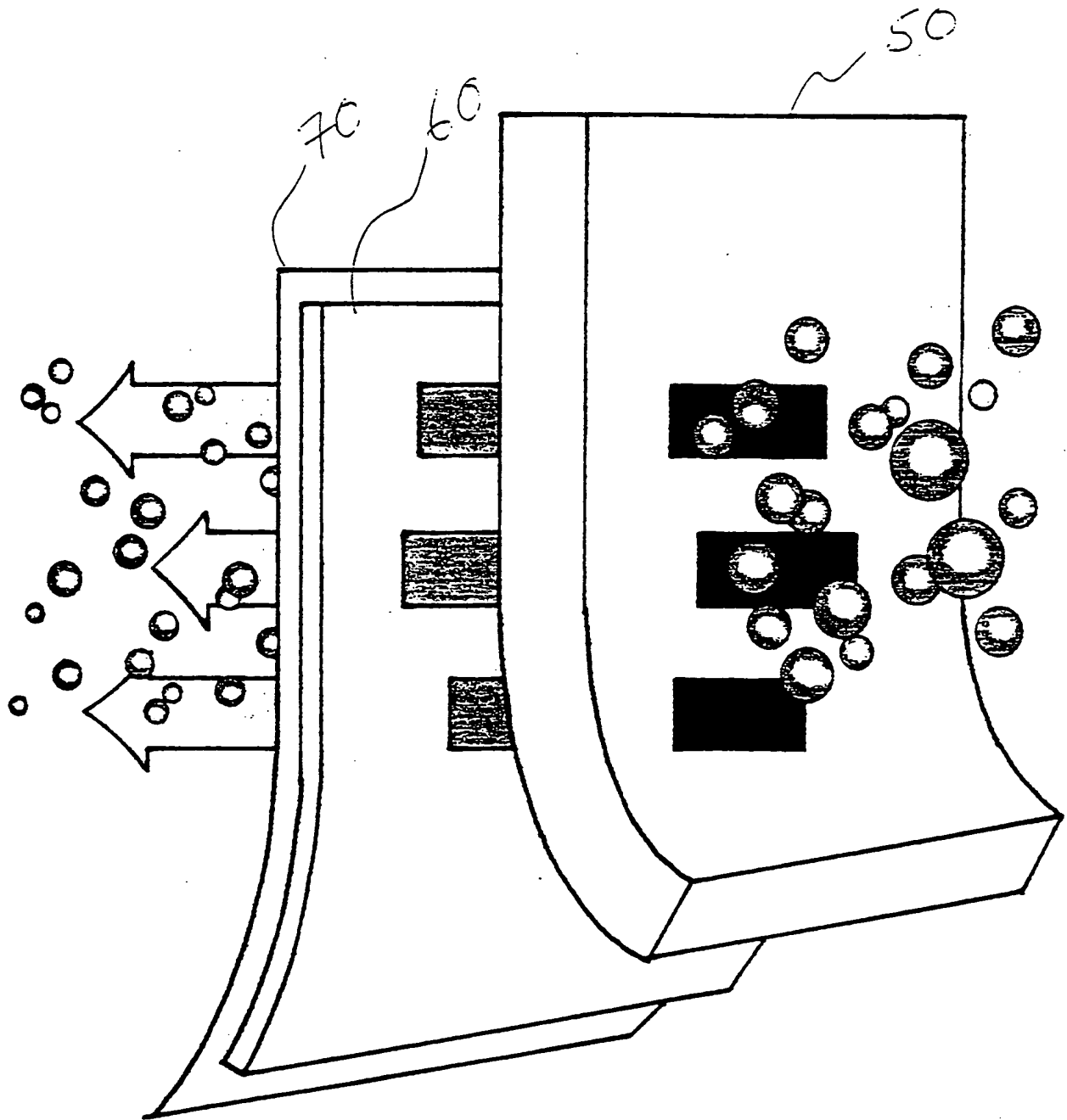


Fig. 2

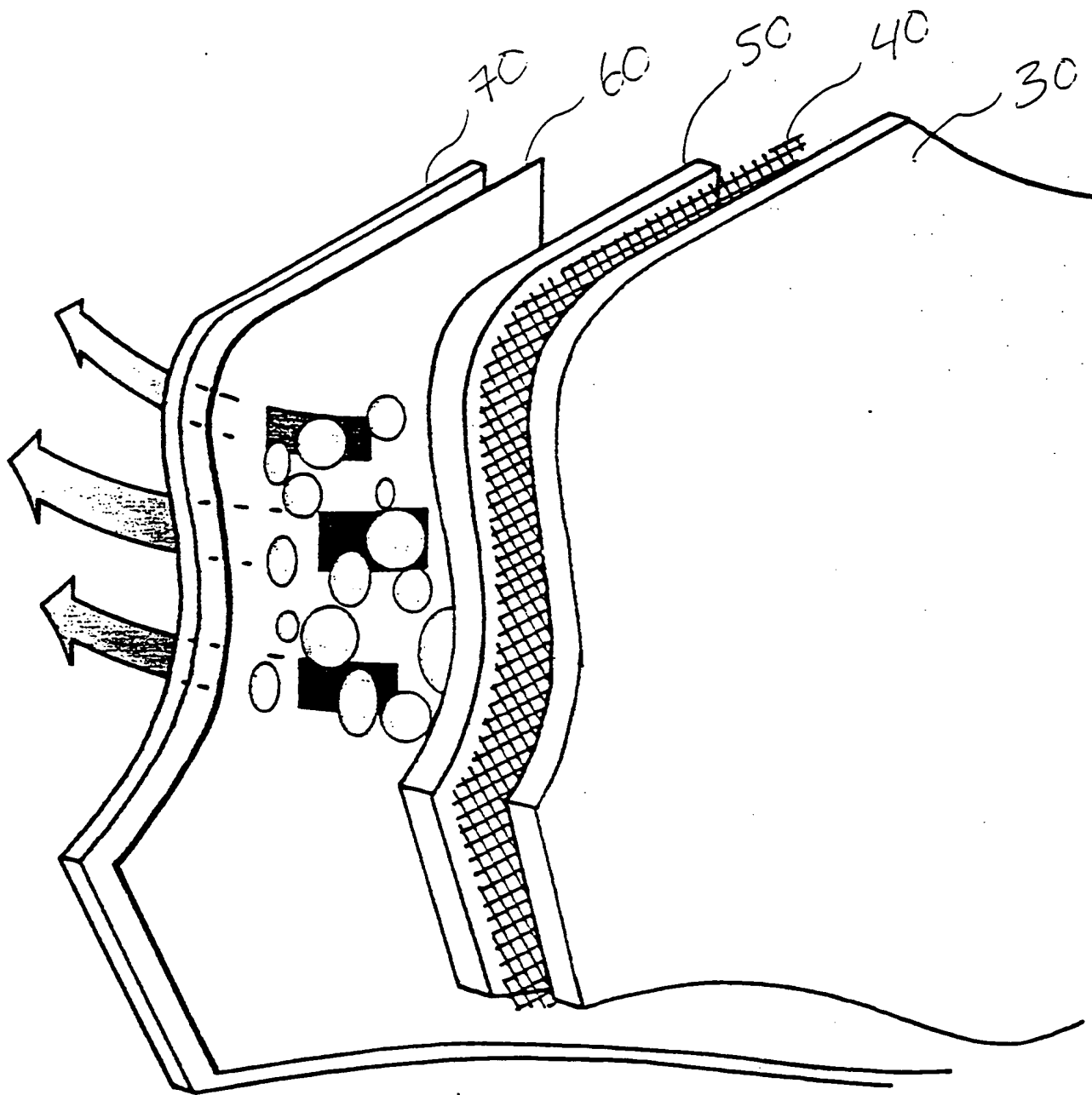


Fig. 3

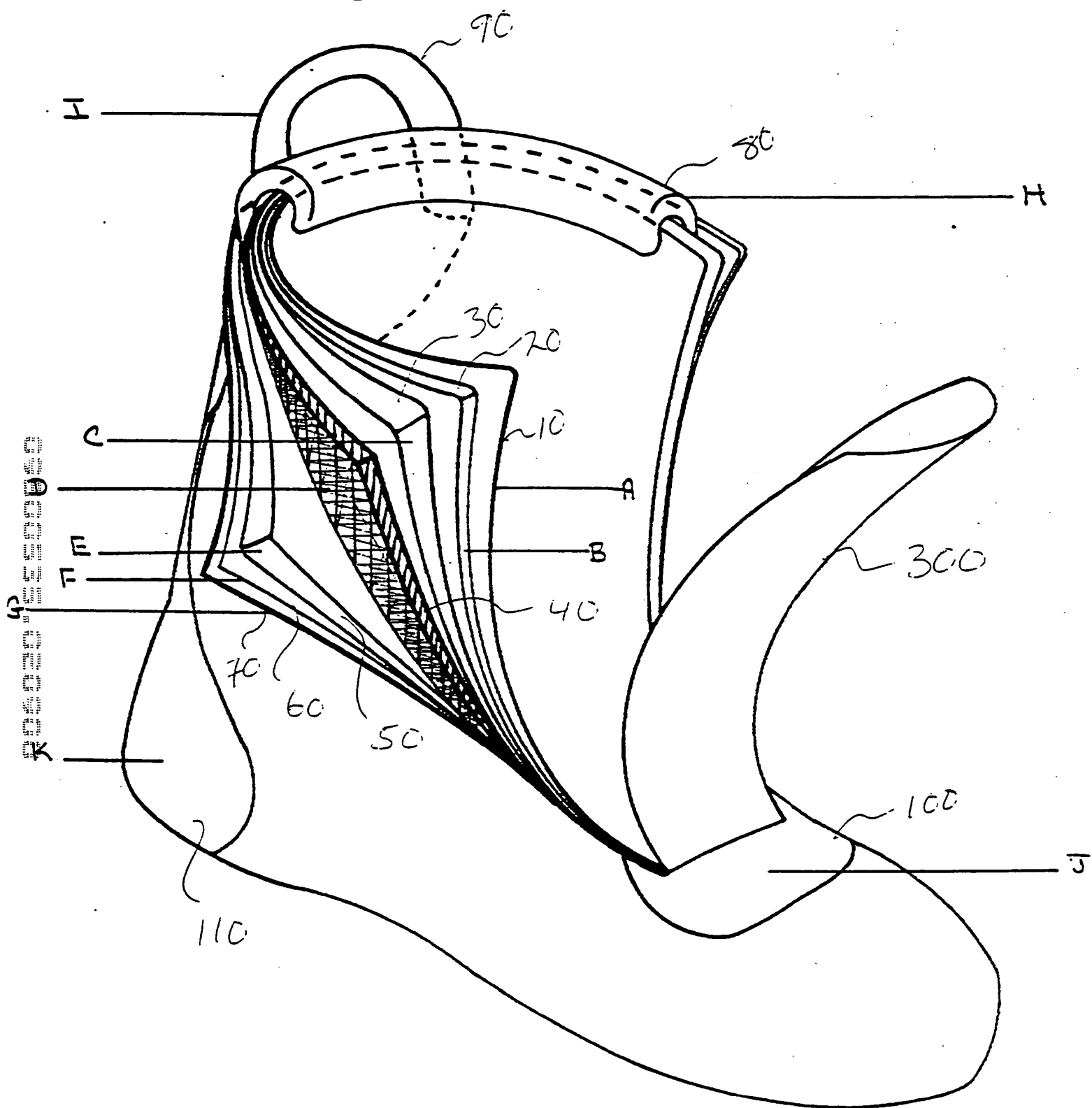


Fig. 4

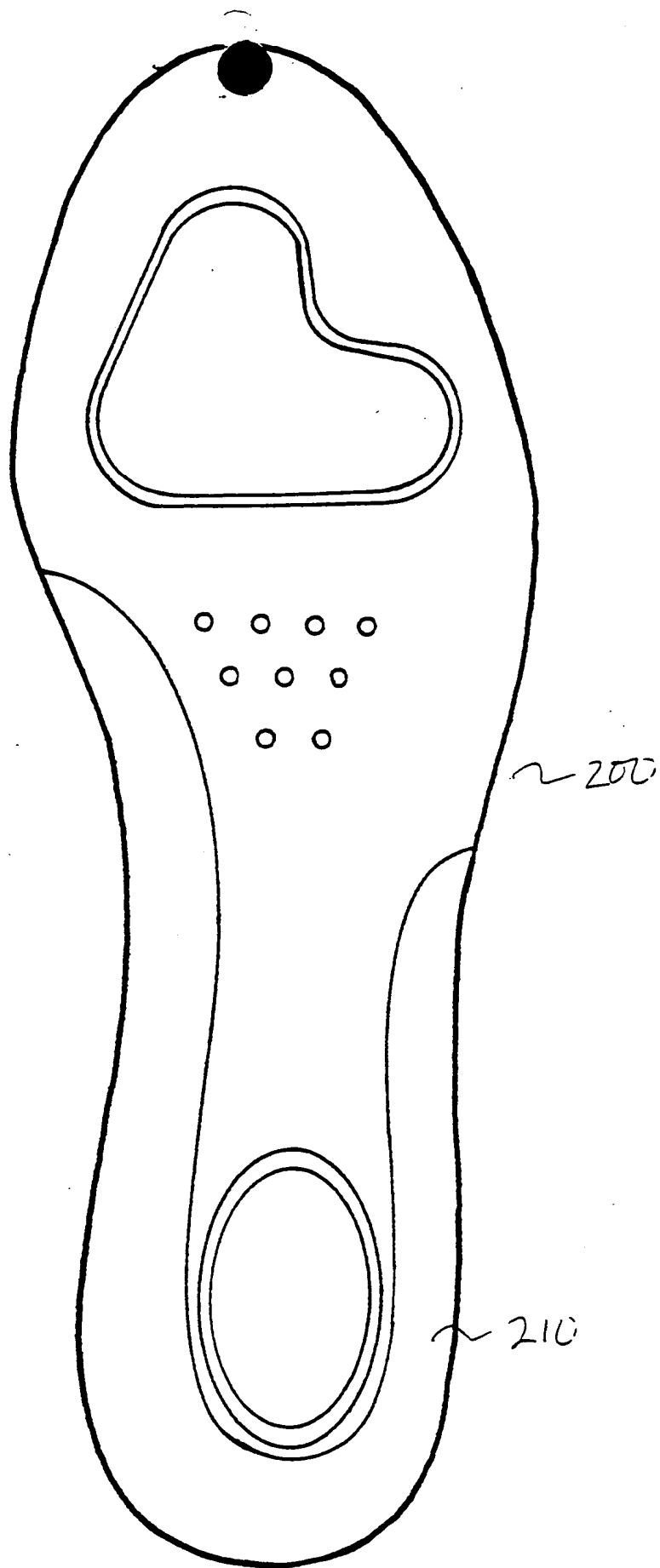


Fig. 5(a)

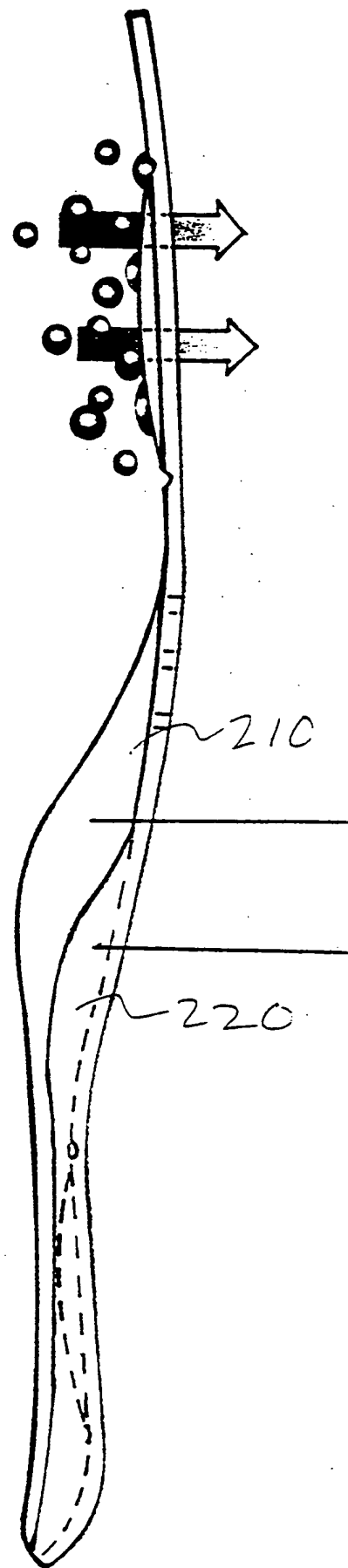


Fig 5(b)

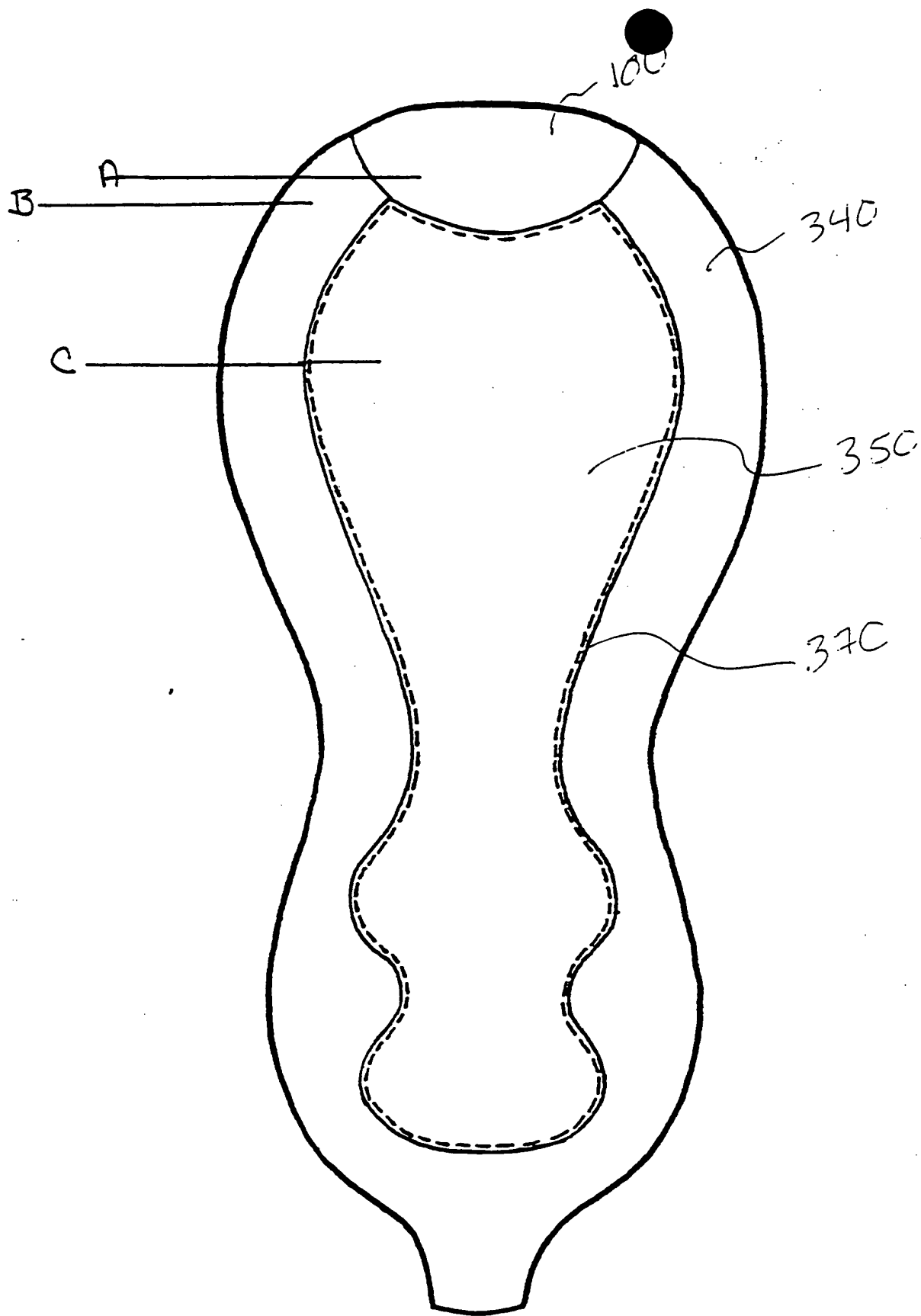


Fig. 6

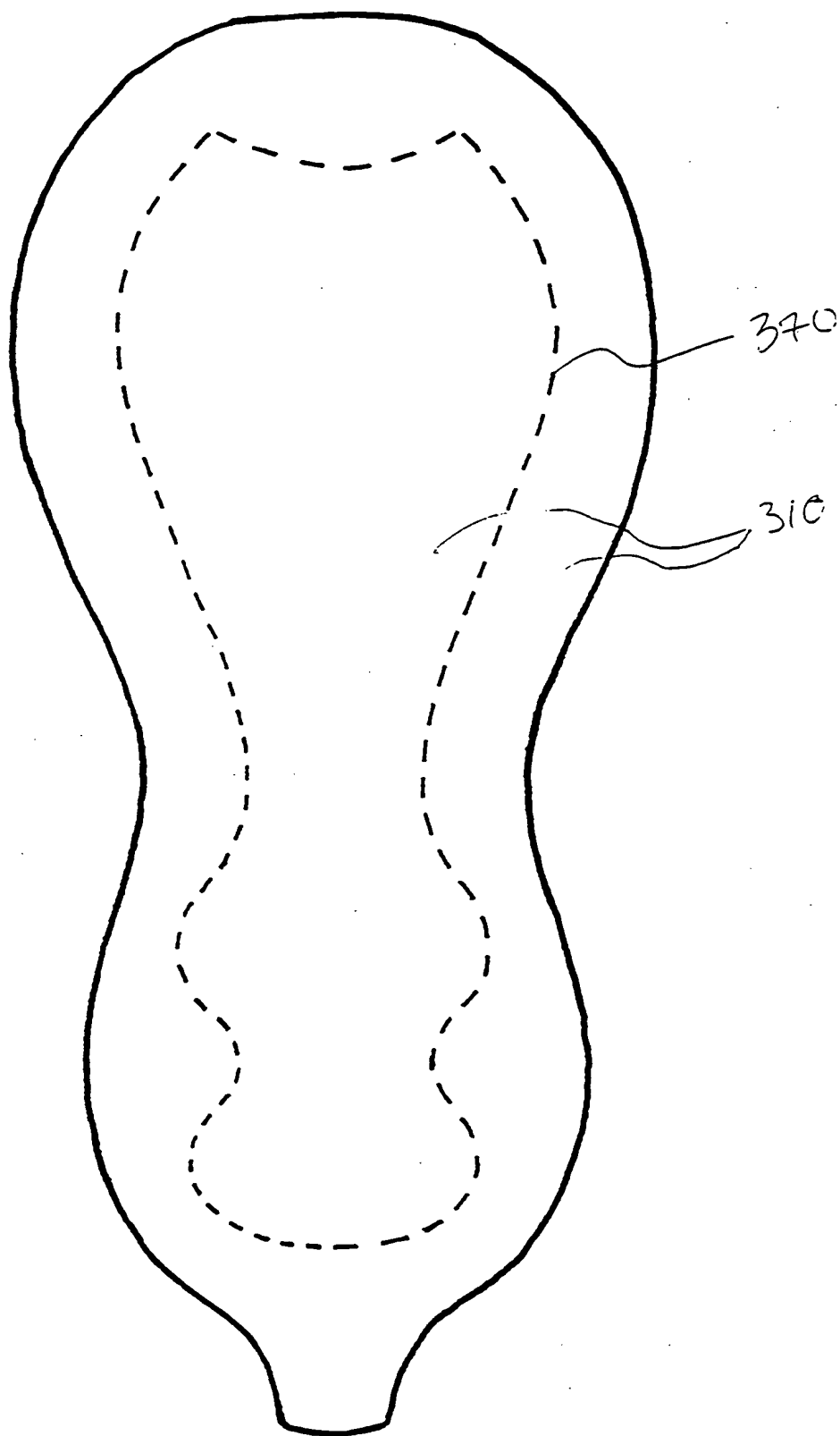


Fig. 7

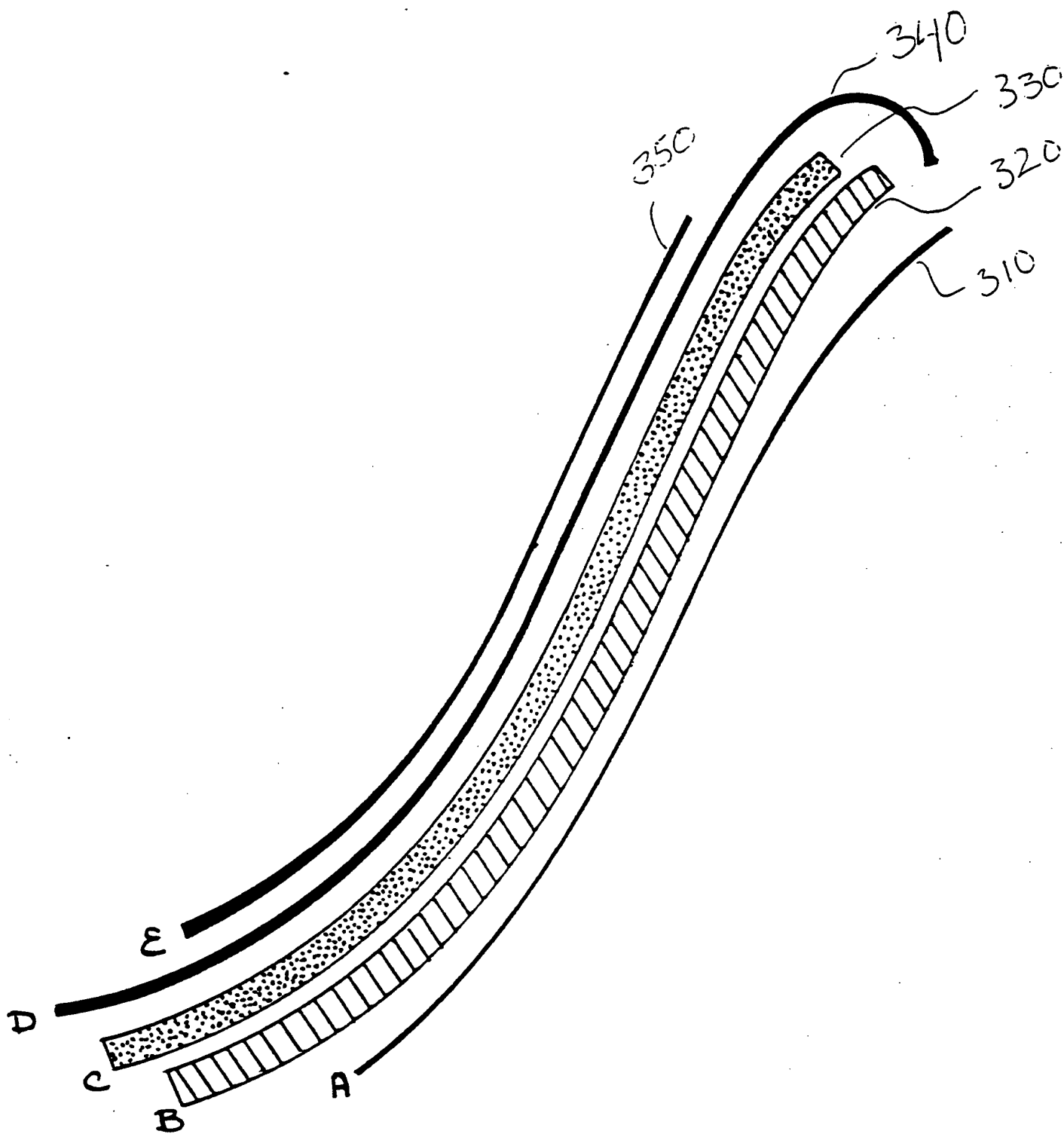


Fig. 8



Fig. 9

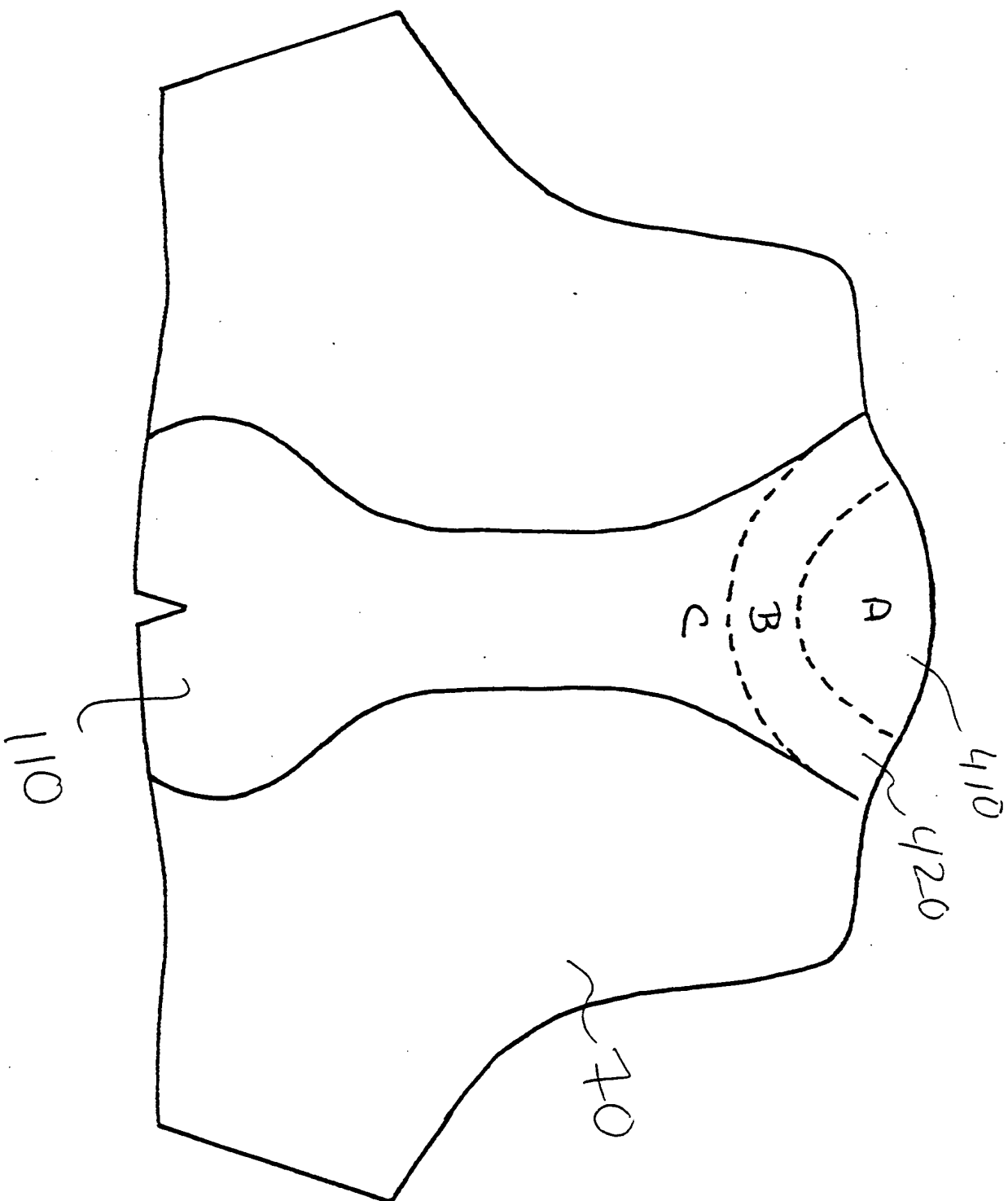


Fig. 10

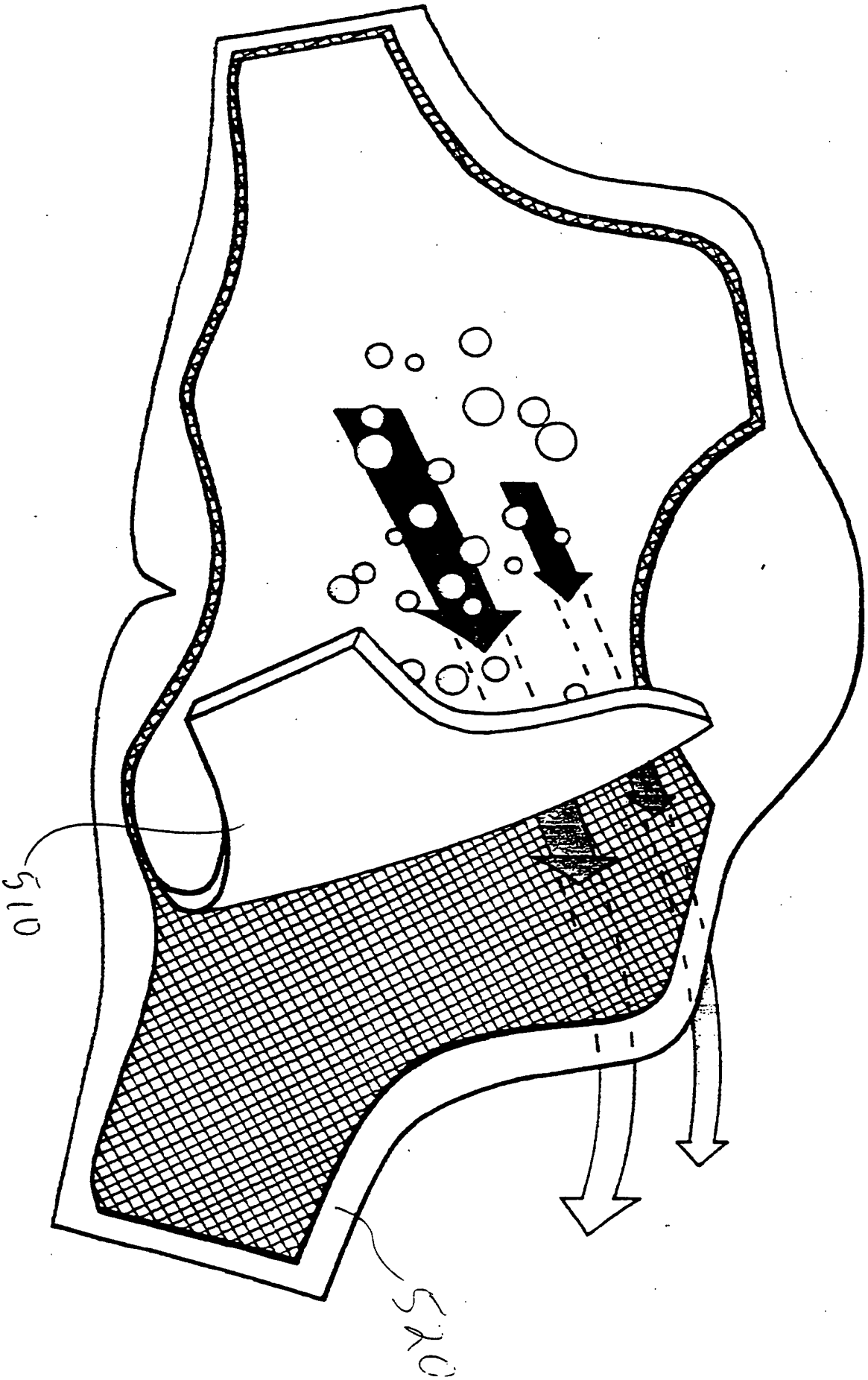


FIG. 10 is a schematic diagram of a device in accordance with the present invention.

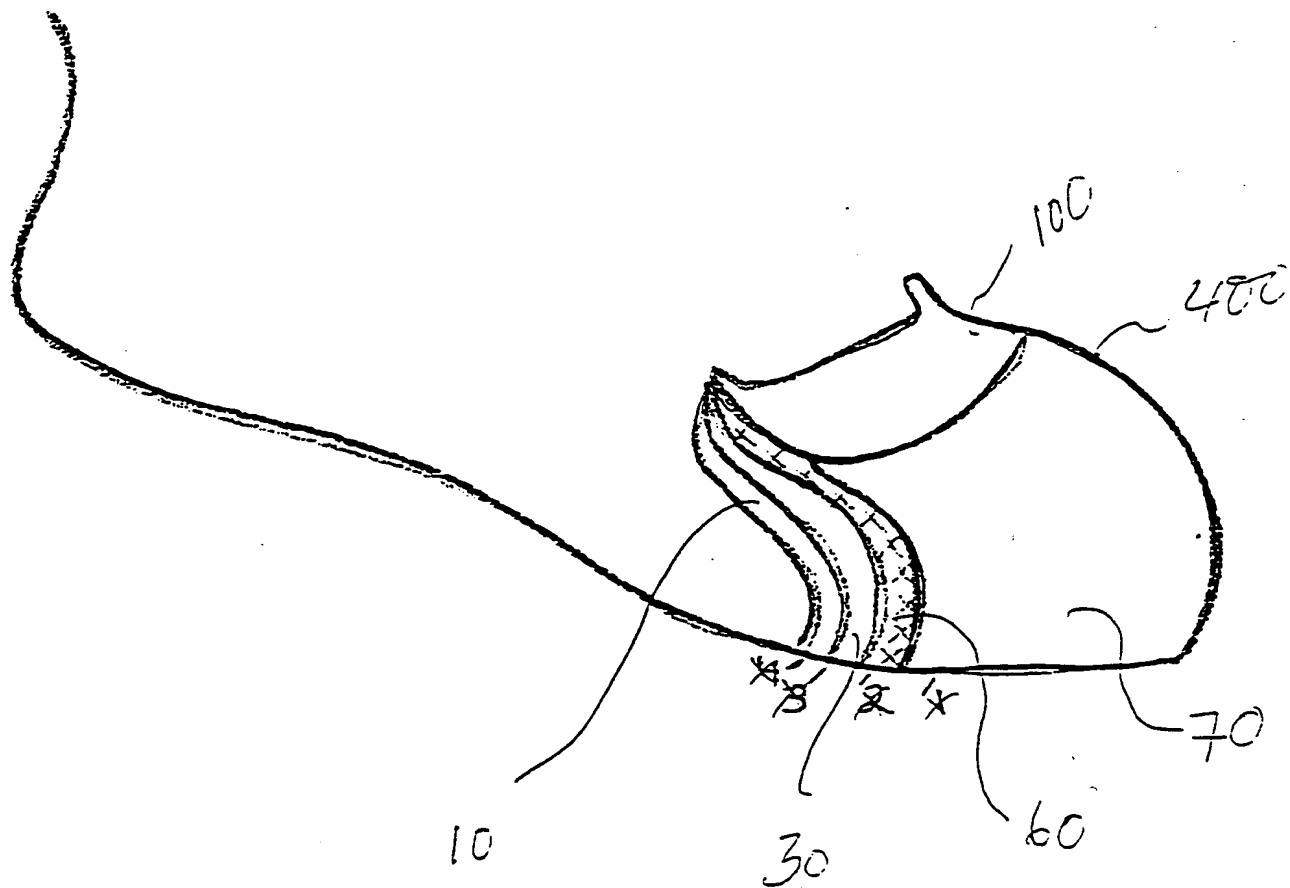


FIG. 11

